

Electronic Acknowledgement Receipt

EFS ID:	5402586
Application Number:	10597919
International Application Number:	
Confirmation Number:	7894
Title of Invention:	Multilayer substrate with built-in-chip-type electronic component and method for manufacturing the same
First Named Inventor/Applicant Name:	Osamu Chikagawa
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Attorney Docket Number:	36856.1458
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	36856-1458-PTO-20090526-US_IDS_Form__SB_08a-.pdf	763337 7b37c20ed73891dd79ea607282c2ec811d b56ad7	no	4

Warnings:

Information:

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2	NPL Documents	36856-1458-PTO-20090527-references.pdf	428500 5464707c42f99f6628d9e28ef0417a2e70c0 64b9	no	10
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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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